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This listing of claims will replace all prior versions, and listings, of claims in the application:

**LISTING OF CLAIMS:**

Claim 1 (canceled)

Claim 2 (previously presented): The delay line according to claim 4, wherein the at least four inductors are defined by a plurality of coil conductor patterns arranged on the same plane of the insulating layers of the laminated body.

Claim 3 (previously presented): The delay line according to claim 4, wherein each of the at least four inductors has a coil axis that is substantially parallel with a laminating direction of the insulating layers of the laminated body, and winding directions of adjacent ones of the at least four inductors are opposite to each other.

Claim 4 (currently amended): A delay line comprising:

~~a coil divided into~~ at least four inductors; and

a laminated body including a plurality of insulating layers and at least four stages of low pass filters including said at least four inductors and a plurality of capacitors; wherein

the insulating layers are made of a dielectric ceramic material having a relative dielectric constant of about 15 or less.

Claim 5 (currently amended): A delay line comprising:

~~a coil divided into~~ at least four inductors; and

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a laminated body including a plurality of insulating layers and at least four stages of low pass filters including said at least four inductors and a plurality of capacitors; wherein

one of the plurality of capacitors is connected to an end of at least one of the at least four inductors and is located on a first insulating layer, and another of the plurality of capacitors is connected to another end of said at least one of the at least four inductors and is located on a second insulating layer, ~~are located at different positions in a laminating direction of the insulating layers; and~~

an insulating layer having said at least one of the at least four inductors is interposed between the first insulating layer and the second insulating layer.

Claim 6 (currently amended): A delay line comprising:

~~a coil divided into~~ at least three inductors; and

a laminated body including a plurality of insulating layers and at least three stages of low pass filters including said at least three inductors and a plurality of capacitors; wherein

the at least three inductors are defined by a plurality of coil conductor patterns arranged on the same plane of the insulating layers of the laminated body; and

a ratio of a vertical dimension to a lateral dimension of each of the coil conductor patterns is approximately 1.

Claim 7 (currently amended): A delay line comprising:

~~a coil divided into~~ at least three inductors; and

a laminated body including a plurality of insulating layers and at least three stages of low pass filters including said at least three inductors and a plurality of capacitors; wherein

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one end of a first of the at least three inductors of a  $k$  stage in the low pass filter and one end of a second of the at least three inductors of a  $k+1$  stage adjacent thereto in the low pass filter are electrically connected to each other on an upper layer of the laminated body, and the second end of the another of the at least three inductors of the  $k+1$  stage in the low pass filter and one end of a third of the at least three inductors of a  $k+2$  stage adjacent thereto in the low pass filter are electrically connected to each other on a lower layer of the laminated body.

Claims 8-9 (canceled)

Claim 10 (currently amended): A delay line comprising:

~~a coil divided into~~ at least four inductors; and

a laminated body including a plurality of insulating layers and at least four stages of low pass filters including said at least four inductors and a plurality of capacitors; wherein

the at least four inductors are defined by a plurality of coil conductor patterns arranged on the same plane of the insulating layers of the laminated body;

a portion comprising the coil conductor patterns is interposed between a first portion comprising at least one of the plurality of capacitors and a second portion comprising another of the plurality of capacitors; and

the insulating layers have a plurality of via holes for connecting the coil conductor patterns that define the at least four inductors.

Claim 11 (previously presented): The delay line according to claim 10, wherein the coil conductor patterns that define the at least four inductors have the same shape.

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Claim 12 (canceled)

Claim 13 (previously presented): The delay line according to claim 4, wherein the insulating layers include magnetic material.

Claim 14 (canceled)

Claim 15 (previously presented): The monolithic circuit array according to claim 20, wherein at least three inductors and at least four capacitors are included in the at least three stages of low pass filters.

Claim 16 (previously presented): The monolithic circuit array according to claim 20, wherein at least four stages of low pass filters are provided in the monolithic laminated body, and at least four inductors and at least five capacitors are included in the at least four stages of low pass filters.

Claim 17 (canceled)

Claim 18 (previously presented): The monolithic circuit array according to claim 20, wherein the insulating layers are made of a dielectric ceramic material having a relative dielectric constant of about 15 or less.

Claim 19 (previously presented): The monolithic circuit array according to claim 20, wherein one of the capacitors is connected to an end of at least one of the inductors, and another of the capacitors is connected to another end of said at least one of the inductors, and the one of the capacitors and the another of the capacitors are

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located at different positions in a laminating direction of the insulating layers.

Claim 20 (currently amended): A monolithic circuit array including a delay line comprising:

~~a coil divided into~~ at least three lumped constant inductors; and  
a plurality of insulating layers stacked on each other to define a monolithic laminated body, the laminated body including at least three stages of low pass filters defined by said at least three lumped constant inductors and a plurality of capacitors; wherein

the at least three lumped constant inductors are defined by a plurality of coil conductor patterns arranged on the same plane of the insulating layers of the laminated body; and

a ratio of a vertical dimension to a lateral dimension of each of the coil conductor patterns is approximately 1.

Claim 21 (previously presented): The monolithic circuit array according to claim 20, wherein the low pass filters are LC  $\pi$  type low pass filters.

Claim 22 (previously presented): The monolithic circuit array according to claim 20, wherein the number of the plurality of capacitors is greater than the number of the lumped constant inductors.

Claim 23 (previously presented): The delay line according to claim 5, wherein the at least four inductors are defined by a plurality of coil conductor patterns arranged on the same plane of the insulating layers of the laminated body.

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Claim 24 (previously presented): The delay line according to claim 5, wherein each of the at least four inductors has a coil axis that is substantially parallel with a laminating direction of the insulating layers of the laminated body, and winding directions of adjacent ones of the at least four inductors are opposite to each other.

Claim 25 (previously presented): The delay line according to claim 5, wherein the insulating layers include magnetic material.

Claim 26 (previously presented): The delay line according to claim 5, wherein the coil conductor patterns that define the at least four inductors have the same shape.

Claims 27-30 (canceled)

Claim 31 (previously presented): The delay line according to claim 10, wherein each of the at least four inductors has a coil axis that is substantially parallel with a laminating direction of the insulating layers of the laminated body, and winding directions of adjacent ones of the at least four inductors are opposite to each other.

Claim 32 (previously presented): The delay line according to claim 10, wherein the insulating layers include magnetic material.

Claim 33 (previously presented): The delay line according to claim 10, wherein the coil conductor patterns that define the at least four inductors have the same shape.

Claims 34-37 (canceled)